U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10037485	12/19/2001	438 257	+55	2811	H. VU

APPLICANTS: Mastromatteo Ubaldo;

CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:

EUROPEAN PATENT OFFICE (EPO) 00830867.8 12/29/2000

PG-PUB DO NOT PUBLISH ☐	RESCIND ☐	
Foreign priority claimed	☐ yes ☐ no	ATTORNEY DOCKET NO
35 USC 119 conditions met	☐ yes ☐ no	
Verified and Acknowledged Examiners's intials		854063.665

TITLE : Process for manufacturing integrated devices having connections on a separate wafer, and integrated device thus obtained

U.S.DEPT. OF COMM./PAT. & TM-PTO-436L(Rev 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
	Assistant Examiner	Total Claims	Print Claim for O.G
ISSUE FEE		**DRAWING**	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
	Primary Examiner		
☐ TERMINAL DISCLAMER	PREPARED FOR ISSUE	Application Examiner	

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